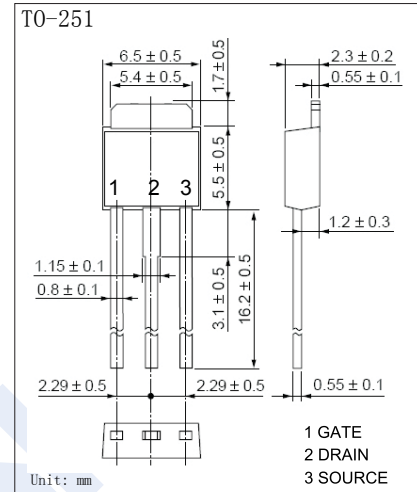
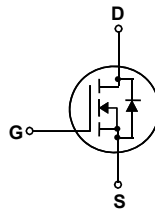


N-Channel Enhancement MOSFET

NDT5N70P

■ Features

- $V_{DS(V)} = 700V$
- $I_D = 5.0 A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 1.8 \Omega$ ($V_{GS} = 10V$)
- High Current, High Speed Switching

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	700	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current	I_D	$T_c=25^\circ C$	5.0
		$T_c=100^\circ C$	3.15
Pulsed Drain Current (Note1)	I_{DM}	20	A
Repetitive Pulse Avalanche Energy (Note2)	E_{AR}	11.2	mJ
Single Pulse Avalanche Energy (Note2)	E_{AS}	186	mJ
Peak Diode Recovery dv/dt (Note3)	dv/dt	4.5	V/ns
Power Dissipation	P_D	$T_c=25^\circ C$	112
		Derate above $25^\circ C$	0.89
Thermal Resistance.Junction- to-Ambient (Note1)	R_{thJA}	110	$^\circ C/W$
Thermal Resistance.Junction- to-Case (Note1)	R_{thJC}	1.12	
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150	

Note.1 Pulse width is based on $R_{\theta JC}$ & $R_{\theta JA}$ and the maximum allowed junction temperature of $150^\circ C$

Note.2 $L=13.8mH$, $I_{AS}=5.0A$, $V_{DD}=50V$, $R_g=25\Omega$, Starting $T_J=25^\circ C$

Note.3 $I_{SD} \leq 5.0A$, $di/dt \leq 200A/us$, $V_{DD} \leq BV_{dss}$, $R_g=25\Omega$, Starting $T_J=25^\circ C$

N-Channel Enhancement MOSFET

NDT5N70P

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =250 μ A, V _{GS} =0V	700			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =700V, V _{GS} =0V			1	μ A
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±30V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250 μ A	2		4	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =2.5A		1.48	1.8	Ω
Forward Transconductance	g _{FS}	V _{DS} =30V, I _D =2.5A		7		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz		893		pF
Output Capacitance	C _{oss}			8.8		
Reverse Transfer Capacitance	C _{rss}			95		
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =560V, I _D =6.0A		18.4		nC
Gate Source Charge	Q _{gs}			3.5		
Gate Drain Charge	Q _{gd}			7		
Turn-On DelayTime	t _{d(on)}	V _{GS} =10V, V _{DS} =350V, I _D =6.0A, R _G =25 Ω		15.2		ns
Turn-On Rise Time	t _r			23.3		
Turn-Off DelayTime	t _{d(off)}			48		
Turn-Off Fall Time	t _f			40		
Body Diode Reverse Recovery Time	t _{rr}	I _F = 6.0A, di/dt= 100A/ μ s		311		nC
Body Diode Reverse Recovery Charge	Q _{rr}			2.5		
Maximum Body-Diode Continuous Current	I _S				5.0	A
Diode Forward Voltage	V _{SD}	I _S =5.0A, V _{GS} =0V			1.4	V

N-Channel Enhancement MOSFET

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Typical Characteristics

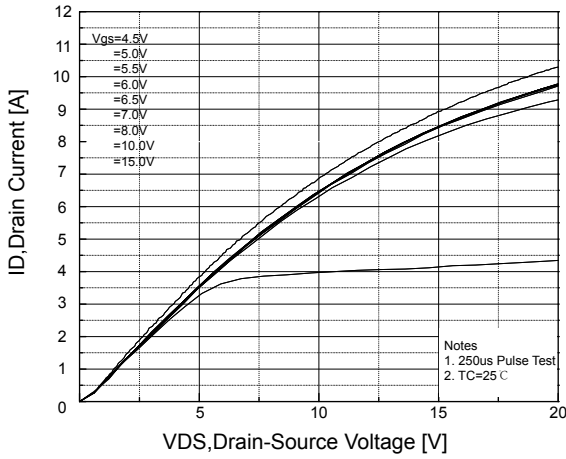


Fig.1 On-Region Characteristics

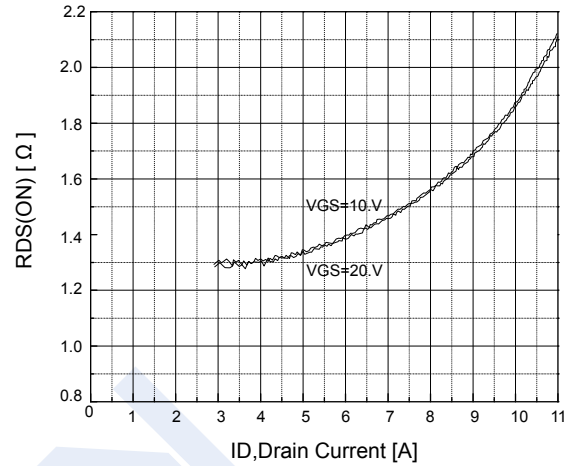


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

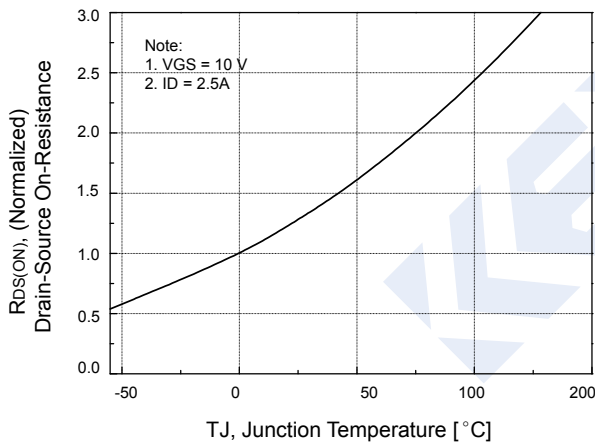


Fig.3 On-Resistance Variation with Temperature

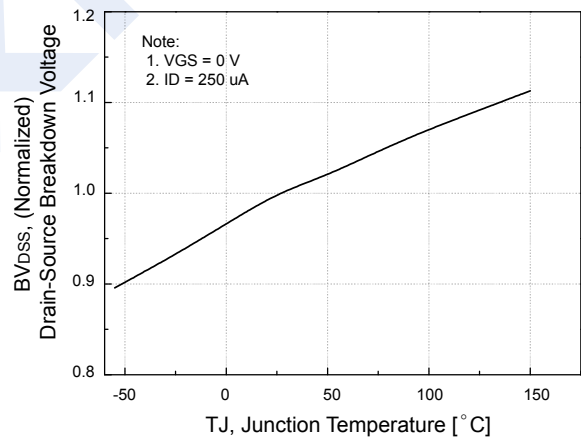


Fig.4 Breakdown Voltage Variation vs. Temperature

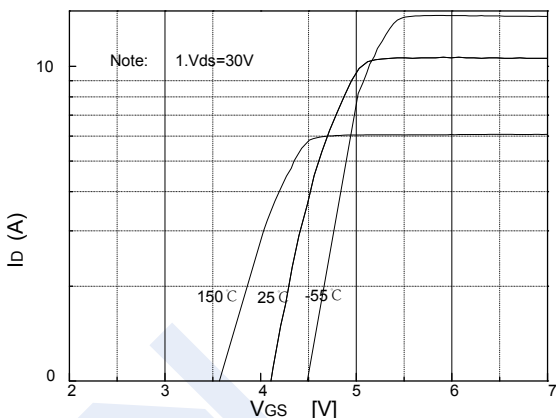


Fig.5 Transfer Characteristics

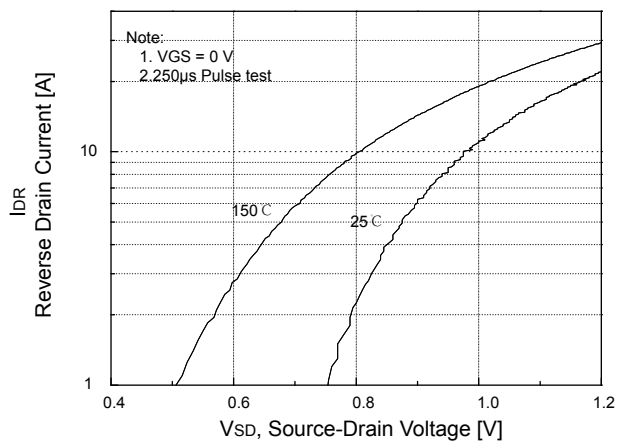


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

N-Channel Enhancement MOSFET

NDT5N70P

■ Typical Characteristics

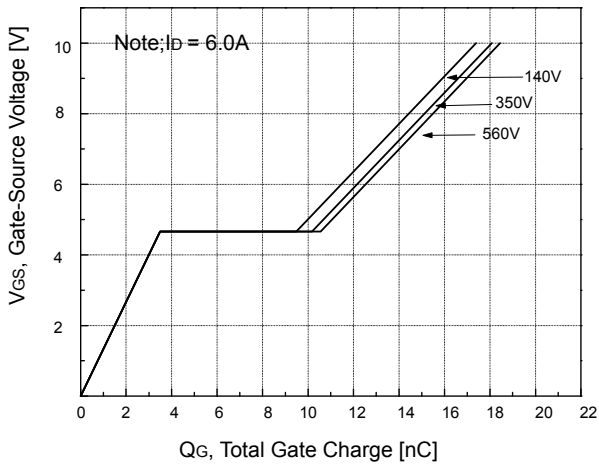


Fig.7 Gate Charge Characteristics

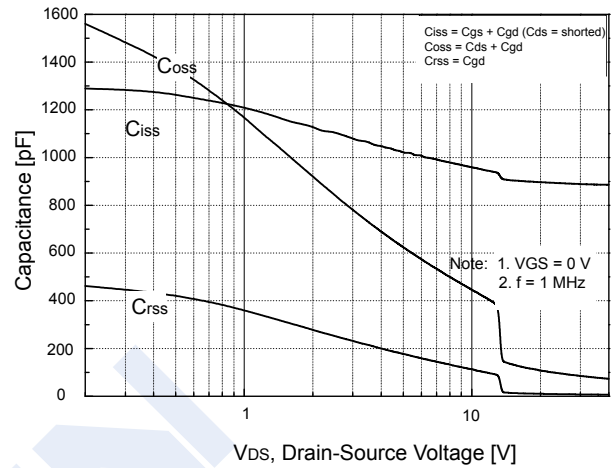


Fig.8 Capacitance Characteristics

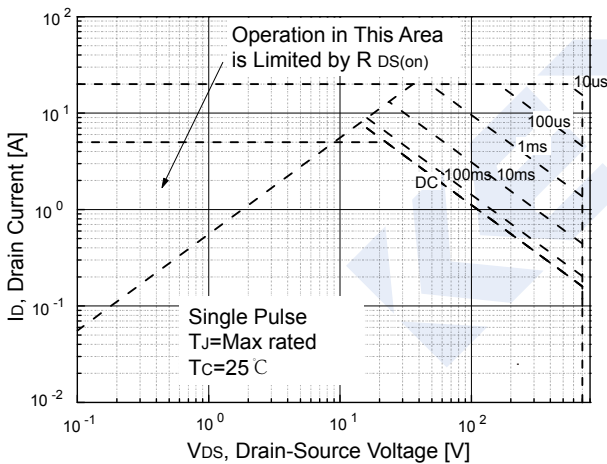


Fig.9 Maximum Safe Operating Area

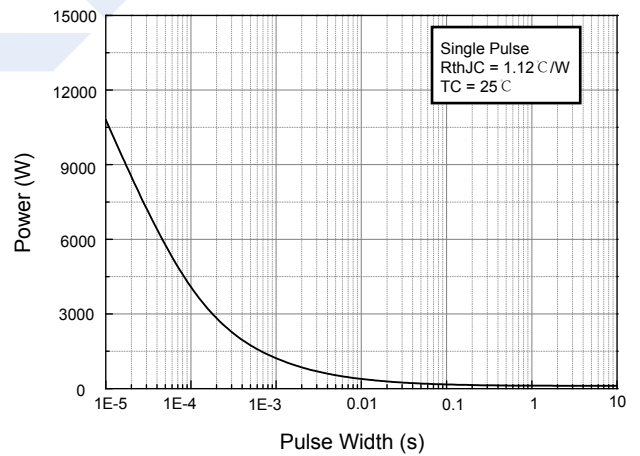


Fig.10 Single Pulse Maximum Power Dissipation

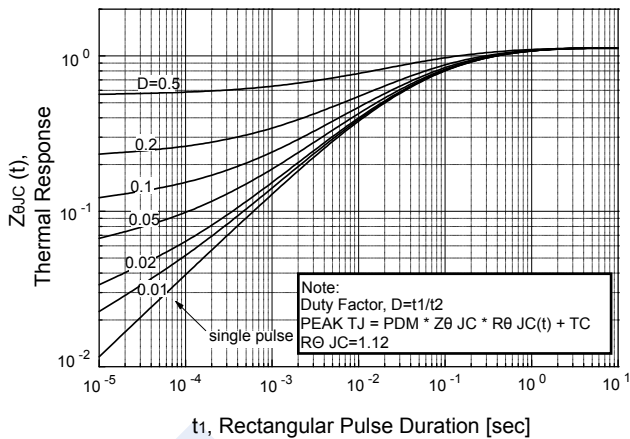


Fig.11 Transient Thermal Response Curve

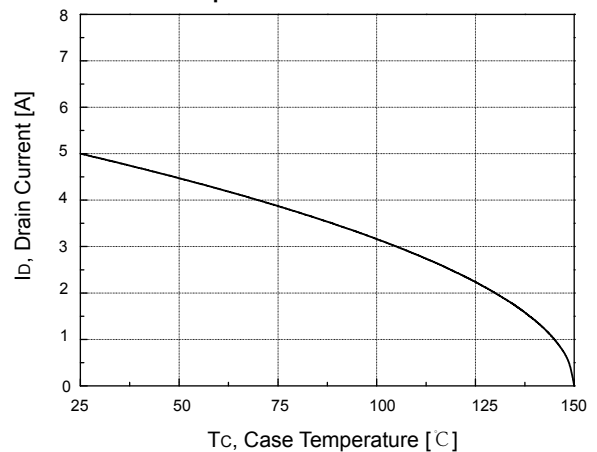


Fig.15 Maximum Drain Current vs. Case Temperature