

## MOS Field Effect Transistor

### 2SK2414

#### ■ Features

- Low On-Resistance

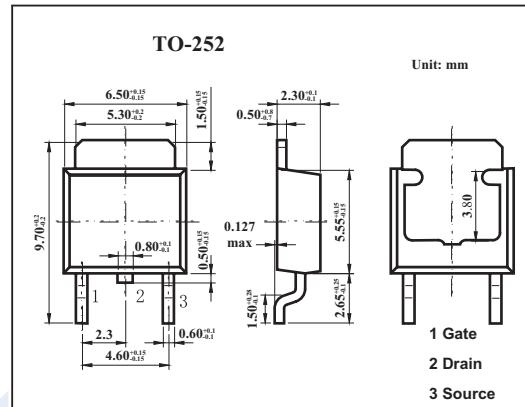
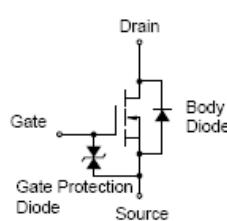
$R_{DS(on)1} = 70 \text{ m}\Omega \text{ MAX. } (@ V_{GS} = 10 \text{ V}, I_D = 5.0 \text{ A})$

$R_{DS(on)2} = 95 \text{ m}\Omega \text{ MAX. } (@ V_{GS} = 4 \text{ V}, I_D = 5.0 \text{ A})$

- Low  $C_{iss}$   $C_{oss} = 840 \text{ pF TYP.}$

- Built-in G-S Gate Protection Diodes

- High Avalanche Capability Ratings



#### ■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain to source voltage	$V_{DSS}$	60	V
Gate to source voltage	$V_{GSS}$	$\pm 20$	V
Drain current	$I_D$	$\pm 10$	A
	$I_{Dp}^*$	40	A
Power dissipation	$P_D$	20	W
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

#### ■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain cut-off current	$I_{DSS}$	$V_{DS}=60\text{V}, V_{GS}=0$			10	$\mu\text{A}$
Gate leakage current	$I_{GSS}$	$V_{GS}=\pm 20\text{V}, V_{DS}=0$			$\pm 10$	$\mu\text{A}$
Gate to source cutoff voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	1.0	1.6	2.0	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS}=10\text{V}, I_D=5\text{A}$	7.0	12		S
Drain to source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=5\text{A}$		52	70	$\text{m}\Omega$
		$V_{GS}=4\text{V}, I_D=5\text{A}$		68	95	$\text{m}\Omega$
Input capacitance	$C_{iss}$	$V_{DS}=10\text{V}, V_{GS}=0, f=1\text{MHz}$		860		pF
Output capacitance	$C_{oss}$			440		pF
Reverse transfer capacitance	$C_{rss}$			110		pF
Turn-on delay time	$t_{on}$	$I_D=5\text{A}, V_{GS(on)}=10\text{V}, R_G=10\Omega, V_{DD}=30\text{V}$		15		ns
Rise time	$t_r$			90		ns
Turn-off delay time	$t_{off}$			75		ns
Fall time	$t_f$			35		ns